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### Clock Oscillators, J-Lead Packages

MODEL	C74	C77
FREQ' RANGE	1.80 to 50.00 MHz	
FREQ' STABILITY	Standard: $\pm 50$ PPM Optional: $\pm 25$ PPM	
OPERATING TEMP' RANGE	-10°C to +70°C -40°C to +85°C	
OUTPUT WAVEFORM	HCMOS/ TTL compatible High Level (VOH): 0.9Vcc Min. Low Level (VOL): 0.1Vcc Max. Symmetry: (50 $\pm$ 10%) or (50 $\pm$ 5%) Rise/ Fall time: 5nSec Max.	
LOAD	15 pF	50 pF
SUPPLY VOLTAGE	5.0 Vdc $\pm$ 10%	3.3 Vdc $\pm$ 10%
SUPPLY CURRENT	25 mA Max. for F < 32.00 MHz 35 mA Max. for frequencies between 32.00 MHz to 50.00 MHz	
ENABLE / DISABLE	Enable: High (above 2.2V) or open Disable: Low (below 0.8V)	
J-LEAD PACKAGE	14.0 X 9.8 X 4.7 mm. 500 pc s min. Tape & Reel	

<b>Environmental Conditions</b>	
<b>SHOCK</b>	IEC 68-2-27 (Test Ea) 30G, 18 mSec, Half Sine
<b>VIBRATION</b>	IEC 68-2-6 (Test Fc), 0.35mm, 5G, 10-2000Hz, 6 cycles/ axis (Fig.2, Table V)
<b>THERMAL SHOCK</b>	IEC 68-2-14 (Test Na), 30 min. in each extreme temperature
<b>SOLDERING HEAT</b>	235°C $\pm$ 5°C for 10 Sec. Max. Solder melting point: 217°C



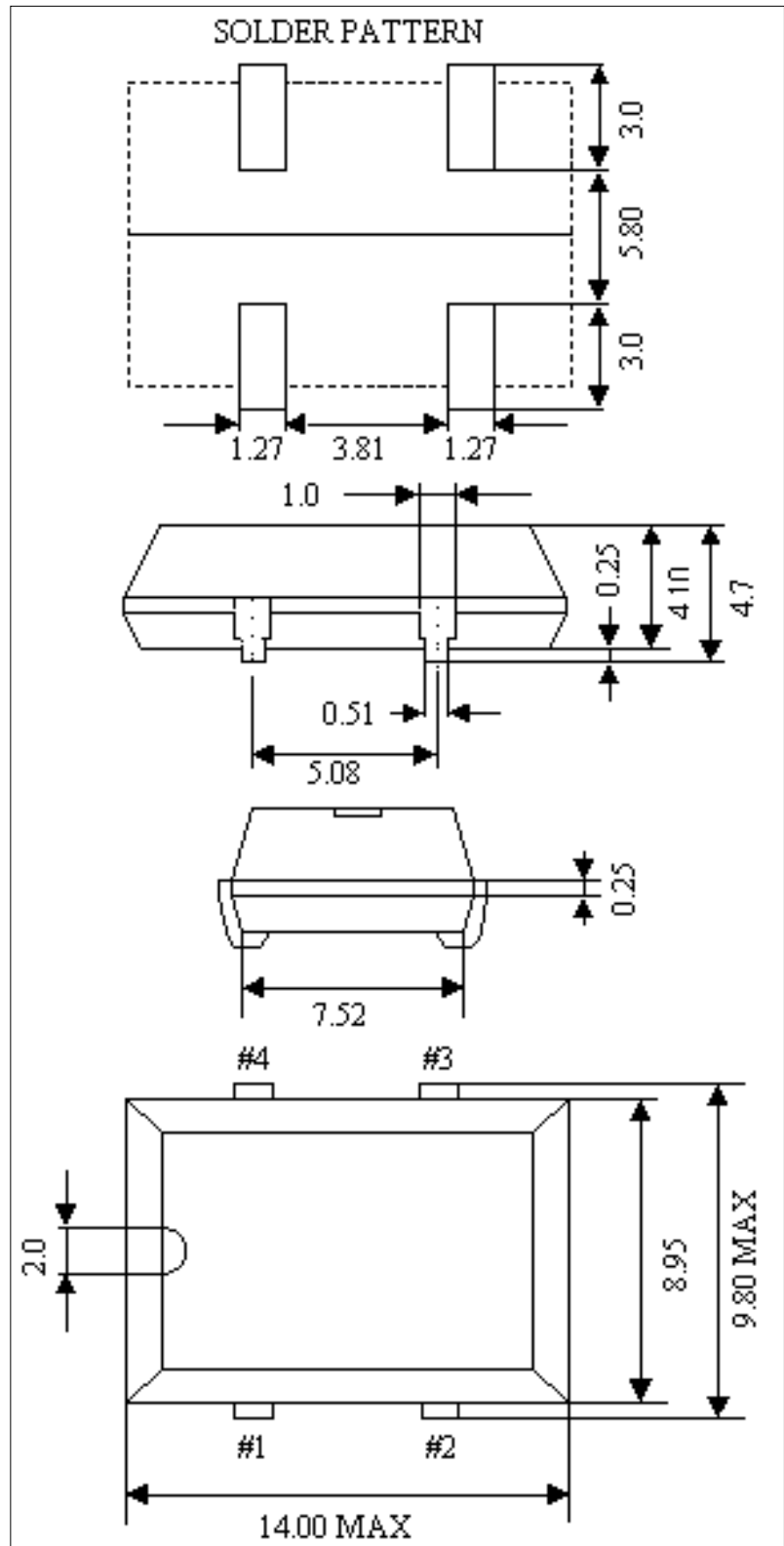
**Tect Electronics**

The authorized distributor in the Greater China Region

## C74, C77

Pin	Function
1	E/D
2	GND
3	Output
4	Vcc

Dimensions in mm.



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